

Slater & Matsil, L.L.P.

Suite 1000
17950 Preston Road
Dallas, Texas 75252-5793
Phone: 972-732-1001 Facsimile: 972-732-9218

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


Applicant: Terletzki, *et al.* **Docket No:** 00P7882US
Serial No.: 09/659,872 **Art Unit:** 2816
Filed: September 13, 2000 **Examiner:** Nguyen, Minh T.
For: Level-Shifting Circuitry Having "High" Output Impedance During Disable Mode

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Respectfully submitted,


Julie Russell
Legal Assistant

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Mail Stop: Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's
Office Action mailed January 7, 2004. Please amend the above-referenced application as
follows.

00P7882US